



## PATENT ABSTRACTS OF JAPAN

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processing delay time, and a transmission characteristic in a burst error is improved.

**(57) Abstract**

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**PURPOSE:** To distribute an error in a small processing delay time and to improve a transmission characteristic by distributing an information bit to (n) system signals, giving each different delay thereto, thereafter, modulating, synthesizing and transmitting them, and executing same reverse processing in a reception side.

**CONSTITUTION:** An information bit is distributed to (n) system signals of two systems, etc., and a delay  $\tau$  is imparted thereto through each different delay '0' and a delaying circuit 11. Thereafter, each information bit of two diffused and modulated, and modulated by diffusing circuit 2, and modulating circuit 3, respectively, and also, synthesized on a time base by a synthesizer and transmitted to a transmission line 5. In a reception side, after executing corresponding demodulation/reverse diffusion, etc., the delay time difference of an information bit train of each system is compensated, thereafter, it is synthesized or selected. By separating timewise this one information bit and distributing and transmitting it, an error is distributed in a small

